

156-657
156 657
Class Subclass
ISSUE CLASSIFICATION

08/163043

5435888

UTILITY SERIAL NUMBER	08/163043	PATENT DATE	JUL 25 1995	PATENT NUMBER	5435888
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SERIAL NUMBER	08/163043	FILING DATE	07/16/95	CLASS	26	SUBCLASS	8	GROUP ART UNIT	1	EXAMINER	Dang
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APPLICANTS ALEX KALNITSKY GRENDEL, INC. HOUSTON, FLAND, TX.

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CONTINUING DATA
VERIFIED

None/00

FOREIGN/PCT APPLICATIONS
VERIFIED

None/00

FOREIGN FILING LICENSE GRANTED 01/06/94

Foreign priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	AS FILED	STATE OR COUNTRY	SHEETS DRWGS.	TOTAL CLAIMS	INDEP. CLAIMS	FILING FEE RECEIVED	ATTORNEY'S DOCKET NO.
35 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	→	FRX	4	25	4	\$894.00	SGS011
Verified and Acknowledged Examiner's initials								

ADDRESS LISA JORGENSON
LEGAL DEPT.
SGS-THOMSON MICROELECTRONICS INC.
1310 ELECTRONICS DRIVE
CARROLLTON, TX 75006

TITLE ENHANCED PLANARIZATION TECHNIQUE FOR AN INTEGRATED CIRCUIT

U.S. DEPT. of COMM.-Pat. & TM Office - PTO-436L (rev. 10-78)

PARTS OF APPLICATION FILED SEPARATELY		Applications Examiner	
NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
1/12/95		Total Claims	Print Claim
21		1	
ISSUE FEE \$1210.00		DRAWING	
Date Paid 11/17/95		Sheets Drwg.	Figs. Drwg.
4		10	2A-2C
Label Area		ISSUE BATCH NUMBER	
THI DANG PRIMARY EXAMINER GROUP 1100		H96	
PREPARED FOR ISSUE			
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STAPLE

AREA

*U.S. GOVERNMENT PRINTING OFFICE: 1994-365-311

PATENT NUMBER

ORIGINAL CLASSIFICATION

CLASS

SUBCLASS

~~456~~ 216~~657~~ 18 WGN

APPLICATION SERIAL NUMBER

CROSS REFERENCE(S)

08/163,043

CLASS

SUBCLASS
(ONE SUBCLASS PER BLOCK)

APPLICANT'S NAME (PLEASE PRINT)

KALNITSKY et al

~~456~~ 663

437

228

231

235

238

216

80

97

IF REISSUE, ORIGINAL PATENT NUMBER

INTERNATIONAL CLASSIFICATION

H 01 L

2100

GROUP
ART UNIT

ASSISTANT EXAMINER (PLEASE STAMP OR PRINT FULL NAME)

1109

PRIMARY EXAMINER (PLEASE STAMP OR PRINT FULL NAME)

THI DALLG

PTO 270
(REV. 5-91)

ISSUE CLASSIFICATION SLIP

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK

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